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August 9, 1991

Dear iPSC® System Administrator:

This package contains the files for your iPSC® system¹ Concurrent File System™ (CFS) Source Code Product, Release 3.3. Any additions or changes to this software are subject to the terms of the Intel Source License Agreement between you and Intel Corporation.

Before using your CFS Source Software:

- **Read this letter completely.**
- **Verify the contents of this package.**
- **Read the *iPSC®/2 and iPSC®/860 System Software Release 3.3 Product Release Notes.***

Package Contents

Please verify that all of the following items are in this package:

Media

iPSC®/2/860 CFS Source Code R3.3
cartridge tape (part number 312104-001)

Documentation

iPSC®/2 and iPSC®/860 Product Release Notes, Release 3.3
iPSC®/2 and iPSC®/860 Source Code Product, Release 3.3 Internal Product Specification
Change Notice (includes Chapter 7, CFS)

If items are missing, or if you have any questions, contact Intel Supercomputer Systems Division immediately. Refer to "Comments and Assistance" for information about how to contact Supercomputer Systems Division.

1. The terms "iPSC Supercomputer" and "iPSC system" refer to any of the following SSD products: iPSC®/2, iPSC®/2S, iPSC®/860, and iPSC®/860S.

What is in This Release?

This release of the source code for the iPSC system CFS software includes detailed documentation for the software design. The documentation includes an Internal Product Specification and a separate "Concurrent File System™" chapter. The separate CFS chapter replaces the empty CFS chapter in the IPS. Release 3.3 of the CFS system source code adds support for the following features:

- **IPD replaces DECON as the iPSC system debugger**
The Interactive Parallel Debugger (IPD) has been incorporated into the iPSC system software as a replacement for the DECON debugger. Chapter 9 of the IPS documents the IPD source code.
- **Performance monitoring support added**
The **prof860** and **PAT** performance monitoring tools have been added with this release. These tools are supported by new compiler switches (**-p** and **-Mperf**), the **rouinit()** and **rouent()** routines, and *object* profiling subdirectories.
- **New i860 C and Fortran compilers added**
The iPSC i860 C compiler (invoked by **icc** on the SRM) and the iPSC i860 Fortran compiler (invoked by **if77** on the SRM) have been added with this release. These are now the recommended compilers (instead of the Green Hills compilers) for all i860 applications.
- **Support added for 64M byte node boards**
Support has been added for RX 64M byte node boards. RX node boards now have memory options of 8M bytes, 16M bytes, 32M bytes, and 64M bytes. The memory options for CX node boards have not changed.
- **VX vector software library (*veclib* and *vxtools*) no longer provided**
The vector software (in *src/veclib* and *src/vxtools*) supporting the VX board is no longer provided. The compatibility versions of this library (*veclib.f77* and *veclib.860*) are still provided with the source product.
- **Message flow control programs added**
Two message flow control programs (**ding** and **dong**) have been added to the source product to aid in debugging the UNIX kernel DCM driver.
- **Accounting software support added**
Routines and parameters have been added to the iPSC system software to support user accounting software. An **account_hook()** routine is now checked by the *commser* process to support user-provided accounting software. An *account* parameter is also now part of the **getcube** command and system calls.
- **Source IPS paragraphs now numbered**
The top three heading levels in the Source IPS document are now numbered to give a more ordered presentation to the information, and to make cross-referencing within the document more usable.
- **Numerous bug fixes incorporated in this release**
Refer to the *iPSC®/2 and iPSC®/860 System Software Release 3.3 Customer Letter* for a complete list of the bugs that have been fixed with this release. The *iPSC®/2 and iPSC®/860 System Software Release 3.3 Release Notes* contains a list of the current bugs for this release.

Installation

Before starting the installation of the R3.3 Source code, make sure you have already installed the following software:

iPSC[®]/860 Compiler Libraries System R3.3
iPSC[®]/860 C Compiler R2.0
iPSC[®]/860 Fortran Compiler R2.0
iPSC[®]/2 System Software R3.3

NOTE

If you currently have a version of either the CFS or NX/2 Source Code product installed on your system, you should remove it before installing the new software. Refer to the section "Remove Add-On Software Packages" in the *AT&T UNIX System V/386 Release 3.2 System Administrator's Guide*. Save any source files that you have modified *prior to* removing the software. After you have successfully installed the Release 3.3 source product, you may integrate your changes into the new code.

Installation Time:	Approximately 40 minutes.
Installation Medium:	1 cartridge tape labeled "iPSC [®] /2/860 CFS Source Code R3.3"
Information you need:	Root password.

CAUTION

The installation process automatically removes the previous version of the source code. This destroys any source files that you have modified.

1. Log in as root.

2. Enter the following:

installpkg

3. When the following message appears:

Are you installing from tape (y/n)?

Enter y.

4. When the following message appears:

Insert Installation Tape in drive and press <RETURN>

A. Insert the iPSC/2/860 CFS Source Code tape in the tape drive.

B. Press <Enter>.

The following message appears:

Searching Tape for list of packages. This may take awhile.
Please wait.

5. When the following message appears:

Available Packages:

1. iPSC CFS Source Code Release 3.3

Do you want to install all of the above packages? <y/[n]>:

Enter y.

The installation is complete when the following message appears:

The installation of the iPSC CFS Source Code Release 3.3 package is now complete.

Refer to Chapter 2 of the *Internal Product Specification* for instructions on how to generate executables from this source code.

Comments and Assistance

Supercomputer Systems Division is eager to hear of your experiences with our new software product. Please call us if you need assistance, have questions, or otherwise want to comment on your iPSC system.

1-800-421-2823 (Customer Support Response Center)
(44) 793 641 469 (in England)
Your Local Intel Sales Office (in Europe)
support@ssd.intel.com (Internet address)

Supercomputer Systems Division is trying to produce the best documentation for your needs. If you have comments about the iPSC manuals, you can send them electronically to the following address:

techpubs@ssd.intel.com (Internet address)

The Intel Supercomputer Users' Group promotes the exchange of information among users. Intel strongly supports the Users' Group and encourages participation in its activities, which include: Special Interest Groups (SIGs), an annual international users' conference, an e-mail task force, and a "freeware" library of user-contributed software, available electronically to all members of the Intel Supercomputer Users' Group. For membership information contact:

JoAnne Wold (503-629-7737)
joanne@ssd.intel.com (Internet address)

Sincerely,



Patrick McNamara

Product Marketing Manager
Intel Supercomputer Systems Division

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